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LOGIC CIRCUIT MODULE HAVING POWER CONSUMPTION CONTROL INTERFACE AND A RECORDING MEDIUM STORING THE MODULE

The present application is a continuation of application Serial No. 5 09/963,494, filed September 27, 2001, the contents of which are incorporated ^{is now a U.S. Patent 6,717,434} ₁ herein by reference.

BACKGROUND OF THE INVENTION

The present invention relates to a logic circuit module and a 10 semiconductor integrated circuit having a recording medium storing the module, and more particularly to a semiconductor integrated circuit having a function of controlling a power consumption of another semiconductor integrated circuit.

An example of a conventional method of lowering a power 15 consumption of a semiconductor integrated circuit is described in JP-A-07-20968. With this method, the consumption power of a computer is reduced by dynamically changing the operating voltage and frequency. Namely, by dynamically lowering the operating voltage and clock speed, the power consumption of the computer system is reduced.

20 An example of an interface to the external is described in JP-A-06-202753 entitled "Method of Stopping Clocks of Computer System and Processor". A clock control signal input is provided in an external interface of a logic circuit. By inputting a signal to the external interface, clocks of a CPU 25 can be stopped at any time irrespective of an instruction currently executed by the processor, while the defined status of the processor is ensured.

In "Integrated Circuit and Computer System" disclosed in JP-A-2000-